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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known	
Application Number	10/624,247
Filing Date	2003-07-21
First Named Inventor	Gurkanwal Sahota
Art Unit	2618
Examiner Name	Vuong, Quochien B.

Sheet

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of

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Attorney Docket No: 990307C1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
001	US5258758	11-02-1993	SOOCH; Navdeep S.		
002	US5515014	05-07-1996	TROUTMAN; Dennis L.		
003	US5712635	01-27-1998	WILSON; James, et al.		
004	US5811984	09-22-1998	LONG; Stephen I., et al.		
005	US5847623	12-08-1998	HADJICHRISTOS; Aristotle		

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ , Number ⁴ , Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆
006	JP04260225	09-16-1992	HITACHI LTD HITACHI VLSI ENG CORP.			<input type="checkbox"/>
007	JP08162942	06-21-1996	MITSUBISHI ELECTRIC CORP.			<input type="checkbox"/>
008	WO9615585A1	05-23-1996	AT & T WIRELESS COMMUNICAT [GB]			<input type="checkbox"/>
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				Examiner Name	Vuong, Quochien B.
Sheet	2	of	3	Attorney Docket No: 990307C1	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2
	009	Analog Devices, "0.8 GHz-2.5 GHz Quadrature Modulator, AD8346", Data Sheet, revision 0, 1999.	<input type="checkbox"/>
	010	Analog Devices, "12-Bit, 125 MSPS High Performance TxDAC D/A Converter, AD9752", Data Sheet, revision 0, 1999.	<input type="checkbox"/>
	011	Analog Devices, "CDMA 3 V Transmitter IF Subsystem with Integrated Voltage Regulator, AD6122", Data Sheet, revision 0, 1998.	<input type="checkbox"/>
	012	International Search Report, PCT/US/28741 International Search Authority EPO, 7/5/01.	<input type="checkbox"/>
	013	KIMURA, K., "MIXER CIRCUITS", WILEY ENCYCLOPEDIA OF ELECTRICAL AND ELECTRONICS ENGINEERING, PP. 324-343, 27 DECEMBER 1999.	<input type="checkbox"/>
	014	KOSUNEN, ET AL., "A CMOS QUADRATURE BASEBAND FREQUENCY SYNTHESIZER/MODULATOR", SOLID-STATE CIRCUITS CONFERENCE, 1998, ESSCIRC '98. PROCEEDINGS OF THE 24TH EUROPEAN SOLID-STATE CIRCUITS CONFERENCE, XP007907207.	<input type="checkbox"/>
	015	Liu, et al., "Complete Mixed-Signal Building Blocks for Single-Chip GSM Baseband Processing", Proceedings of the 1998 Custom Integrated Circuits Conference, pp. 101-104, 1998.	<input type="checkbox"/>
	016	Minogue, P., "A 3 V GSM Codec", Solid-State Circuits, IEEE Journal of Solid State Circuits, Vol. 30, Issue 12, pp. 1411-1420, 1995.	<input type="checkbox"/>
	017	Neff, "Automatic synthesis of CMOS Digital/Analog Converters", EECS Department, University of Berkeley, Technical Report No. UC/ERL M95/28, Ch. 1, 2 and 6, 1995.	<input type="checkbox"/>
	018	PEARSE, ROBERT A., "VOLTAGE REFERENCES", WILEY ENCYCLOPEDIA OF ELECTRICAL AND ELECTRONICS ENGINEERING, PP. 344-352, 27 DECEMBER 1999.	<input type="checkbox"/>
	019	Silicon Germanium CDMA transmit/ Dual PLL-RFIC Cx74002 Conexant Delivers RF Chipsets for 2G and 3G CDMA Applications, Pages 2.	<input type="checkbox"/>
	020	SONG, B., "DIGITAL-TO-ANALOG CONVERSION", WILEY ENCYCLOPEDIA OF ELECTRICAL AND ELECTRONICS ENGINEERING, PP. 518-530, 27 DECEMBER 1999.	<input type="checkbox"/>
	021	Wong, C., "A 3-V GSM Baseband Transmitter", IEEE Journal of Solid-State Circuits, Vol. 34, No. 5, pp. 725-730, May 1999.	<input type="checkbox"/>
			<input type="checkbox"/>

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